

Preliminary Product Description TCP/easy GPIMC

- General Purpose Interface Microcontroller Connectivity
- TCP/IP v.4
- UDP, ICMP, ARP, DLC, MAC
- 4 independent connections
- Up to 4-5 Mbps/s
- 10/100 Mbps Mode
- EEPROM for MAC Address on Board
- I²C Interface



Product functionality of the TCP/easy:

TCP/easy is a low budget development tool which allows the connection of virtually any commonly used MCU to the **TCP/ Internet Protocol**. The easy to set up communication through a general MCU Port via the W3100A chip and PHY to the PC with RJ45 interface allows to concentrate on the application instead of struggling with layouts, signal flows and possible hardware bugs.

Processor interface

- **Parallel**
Processor bus (D0..D7, A0..A14, RD, WR)
Bus mode of W3100A to select (clocked mode, ext. clocked mode, non clocked mode)
Direct Addressing mode (32 Kbytes within the MC-Address space needed)
Indirect Addressing mode (Only 2 Byte at MC-Address space needed)
- **I²C**
EEPROM for configuration data
- **Serial**
I²C – Interface (100 Khz Mode und 400 Khz Mode)
- **Lan**
RJ45 Connector
- **Power**
3,3 V Vcc, 5V tolerance Signal processing, max 80 mA current

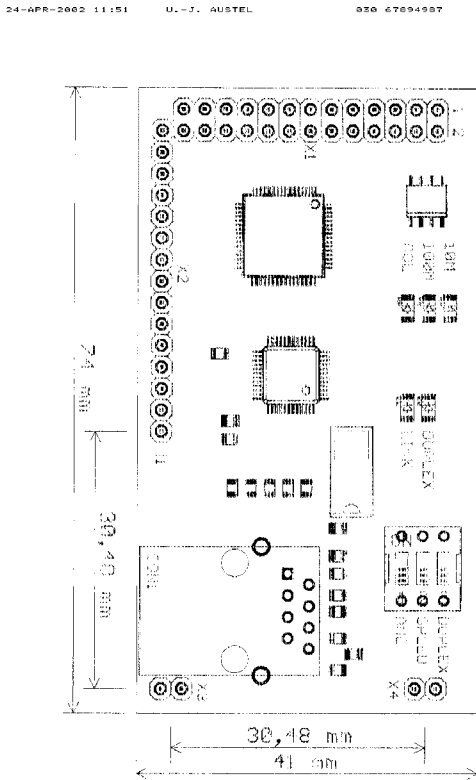
Dimensions

- 75 mm x 40 mm
- Signal connectors with standard 2,54 mm pin headers

Technical Changes and enhancements for this product may be made with any notice in advance.

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Description of Signals:



X1	X2	X2	X2
1 3,3V	2 GND	27 GND	
3 SDA	4 SCL	28 D7	
5 WR	6 IRQ	29 D6	
7 CS	8 RD	30 D5	
9 RS CHIP	10 GND	31 D4	
11 A13	12 A14	32 D3	
13 A11	14 A12	33 D2	
15 A9	16 A10	34 D1	
17 A7	18 A8	35 D0	
19 A5	20 A6	36 MODE 0	
21 A4	22 GND	37 MODE 1	
23 A2	24 A3	38 EXT_CLK	
25 A0	26 A1	39 RESPHY	
		40 3,3V	
X3/X4	GND	41 GND	

Backside				
Switches	SW2	SW3	SW4	EEPROM
		SW5	SW6	I2Chip
MC	Interface - Settings			

Example Circuit

